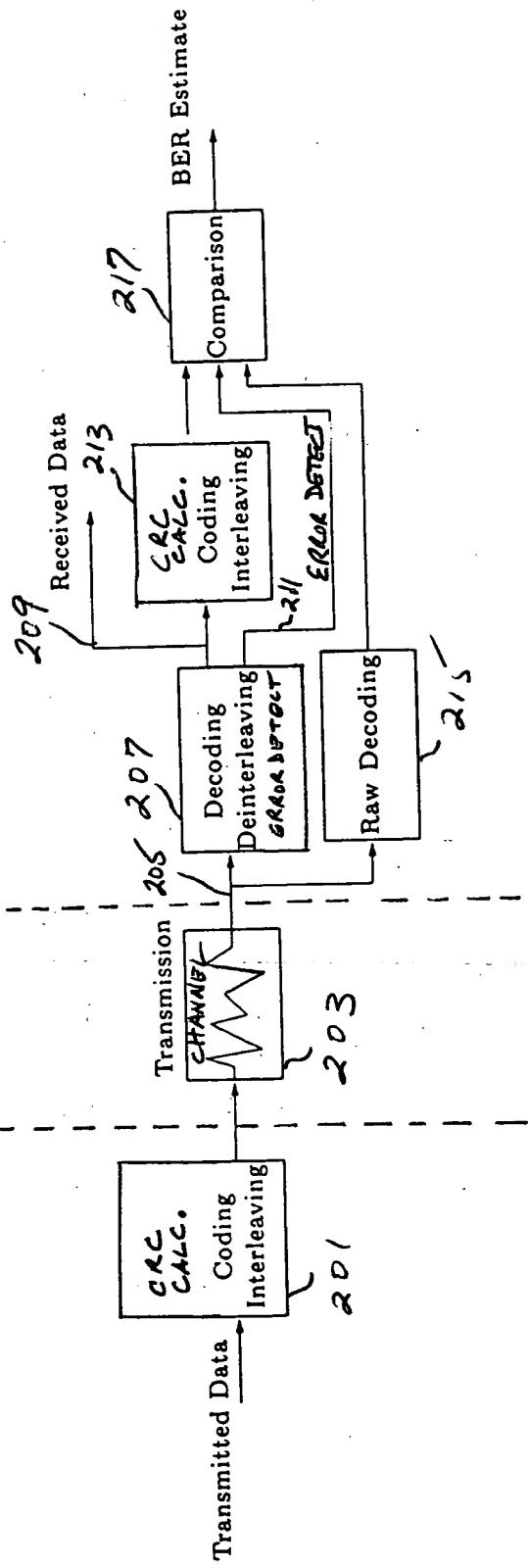


Fig. 1

DATA FLOW DIAGRAM

TRANSMITTER

RECEIVER



F16. 2

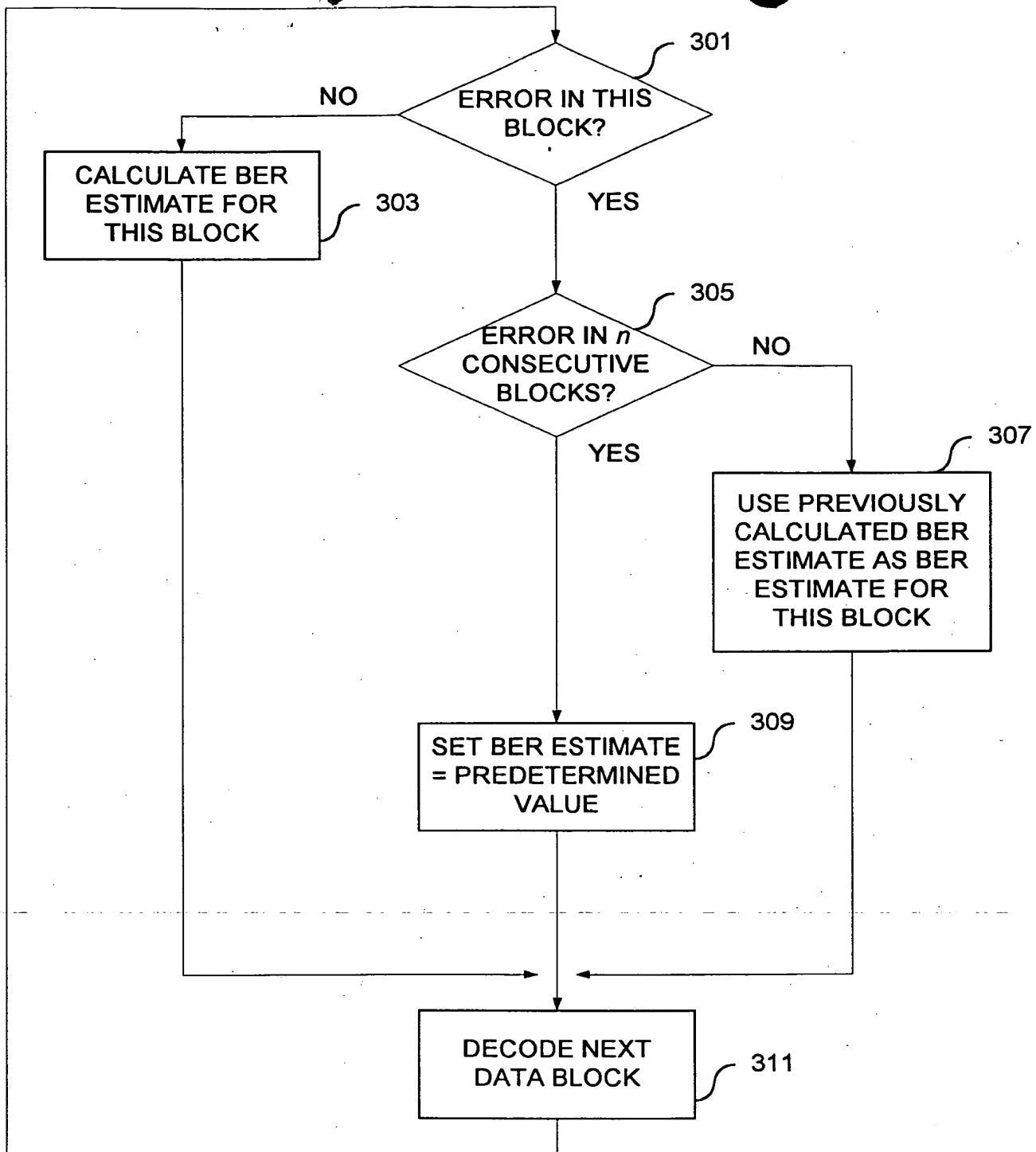


FIG. 3